

# 6th & 7th CIES Technology Forum VIRTUAL

March 22 (Tue) – 23 (Wed), 2022

## International Symposium

March 22 (Tue) **DAY 1**

[http://www.cies.tohoku.ac.jp/7th\\_forum/](http://www.cies.tohoku.ac.jp/7th_forum/)

### Part 1 [Live] 9:00-12:10 (JST)

JST	PDT	CET		
9:00-9:05	March 21 17:00-17:05	1:00-1:05	Opening remarks	Tetsuo Endoh (Tohoku Univ.)
9:05-9:35	17:05-17:35	1:05-1:35	<b>Invited talk 1</b> Potential of Spintronics Memory and Interesting Usage Case	Shigeki Tomishima (Intel)
9:35-10:05	17:35-18:05	1:35-2:05	<b>Invited talk 2</b> Toward low power and high speed SOT-MRAM	Mingyuan Song (TSMC)
10:05-10:35	18:05-18:35	2:05-2:35	<b>Invited talk 3</b> Development of embedded STT-MRAM for various application and scaling	Seungpil Ko (Samsung)
10:35-11:05	18:35-19:05	2:35-3:05	<b>Invited talk 4</b> Embedding STT MRAM into high performance, low leakage micro controller applications	Thomas Jew (NXP)
11:05-11:35	19:05-19:35	3:05-3:35	<b>Invited talk 5</b> Extending GaN integration to higher power and faster speeds – an examination of the progress and roadmaps for GaN integration	Alex Lidow (Efficient Power Conversion)
11:35-12:05	19:35-20:05	3:35-4:05	<b>Invited talk 6</b> Integrated power electronics technology applying wide-bandgap devices	Yoshikazu Takahashi (Tohoku Univ.)
12:05-12:10	20:05-20:10	4:05-4:10	Closing remarks	Tetsuo Endoh (Tohoku Univ.)

### Part 2 [Streaming] 17:00-20:10 (JST)

JST	PDT	CET		
17:00-17:05	1:00-1:05	9:00-9:05	Opening remarks	Tetsuo Endoh (Tohoku Univ.)
17:05-17:35	1:05-1:35	9:05-9:35	<b>Invited talk 1</b> Potential of Spintronics Memory and Interesting Usage Case	Shigeki Tomishima (Intel)
17:35-18:05	1:35-2:05	9:35-10:05	<b>Invited talk 2</b> Toward low power and high speed SOT-MRAM	Mingyuan Song (TSMC)
18:05-18:35	2:05-2:35	10:05-10:35	<b>Invited talk 3</b> Development of embedded STT-MRAM for various application and scaling	Seungpil Ko (Samsung)
18:35-19:05	2:35-3:05	10:35-11:05	<b>Invited talk 4</b> Embedding STT MRAM into high performance, low leakage micro controller applications	Thomas Jew (NXP)
19:05-19:35	3:05-3:35	11:05-11:35	<b>Invited talk 5</b> Extending GaN integration to higher power and faster speeds – an examination of the progress and roadmaps for GaN integration	Alex Lidow (Efficient Power Conversion)
19:35-20:05	3:35-4:05	11:35-12:05	<b>Invited talk 6</b> Integrated power electronics technology applying wide-bandgap devices	Yoshikazu Takahashi (Tohoku Univ.)
20:05-20:10	4:05-4:10	12:05-12:10	Closing remarks	Tetsuo Endoh (Tohoku Univ.)

**Organizer:** Center for Innovative Integrated Electronic Systems (CIES), Tohoku University

**Co-sponsored:** Research Institute of Electrical Communication (RIEC), Tohoku University

**Sponsors:** CAO, MEXT, METI, JPO, JSPS, JST, NEDO, INPIT