

Bridge to the Sustainable Growth with Innovative Integrated Electronic Systems

*From super low power edge computing
and car electronics to AI systems*



4th

CIES

Technology

Forum

March 22 - 23, 2018

DAY 2

CIES Progress Report

March 23, 2018

Station Conference Tokyo,
Sapia Tower 5F 501ABS
1-7-12 Marunouchi, Chiyoda-ku, Tokyo

Welcome address	Susumu Satomi (Tohoku Univ.)
Address	Kazuo Kyuma (CSTI) Futoshi Sano (MEXT) Fumikazu Sato (METI) Takashi Yamashita (JPO)
CIES overview	Tetsuo Endoh (Tohoku Univ.)
Progress report	Yasuo Ando (Tohoku Univ.) Tetsuo Endoh (Tohoku Univ.) Takahiro Hanyu (Tohoku Univ.) Shoji Ikeda (Tohoku Univ.) Koichi Ito (Tohoku Univ.) Yasunobu Mizomoto (Tohoku Univ.) Tetsuya Suemitsu (Tohoku Univ.)
Closing remarks	Tetsuo Endoh (Tohoku Univ.)
Sponsors	CAO, MEXT, METI, JPO, JSPS, JST, NEDO, INPIT

Registration

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4th_forum/entry.html](http://www.cies.tohoku.ac.jp/4th_forum/entry.html)

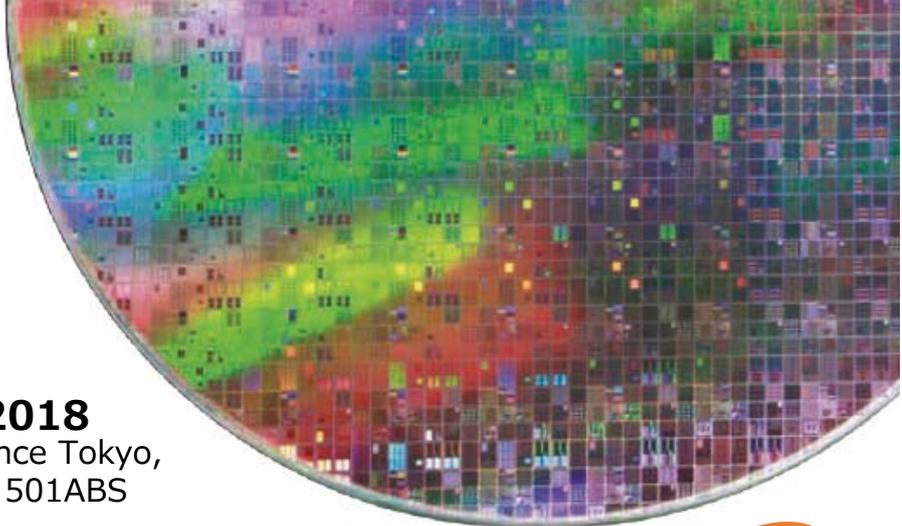
Registration fee: Free
Pre-registration deadline:
March 15, 2018

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**Progress
Report**

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DAY 2

<<<Registration desk open at 9:15am>>>

10:00-10:30	Welcome address	Susumu Satomi (Tohoku Univ.)
	Address Council for Science and Technology and Innovation, CAO	Kazuo Kyuma (CSTI)
	Address Director-General, Science and Technology Policy Bureau, MEXT	Futoshi Sano (MEXT)
	Address Deputy Director-General, Industrial Science and Technology Policy and Environment Bureau, METI	Fumikazu Sato (METI)
	Address Director-General, 4th Patent Examination Department, JPO	Takashi Yamashita (JPO)
10:30-11:10	CIES Overview	Tetsuo Endoh (Tohoku Univ.)
11:10-11:50	Industry-academic collaboration STT-MRAM aimed at developing non-volatile working memory and its manufacturing technologies	Shoji Ikeda (Tohoku Univ.)
	Innovative material and device development in STT-MRAM	Tetsuo Endoh (Tohoku Univ.)
	New evolution of STT-MRAM to non-volatile AI chip	
11:50-13:20	Lunch	
13:20-13:50	Industry-academic collaboration Technologies to automatically design environments for low energy consumption and highly functional VLSI processors based on non-volatile memory	Takahiro Hanyu (Tohoku Univ.)
13:50-14:20	Industry-academic collaboration Embedded security technology	Koichi Ito (Tohoku Univ.)
14:20-14:50	JSPS Core-to-Core (Japan – UK – France) Creation of international research hub for innovative semiconductor materials and devices	Tetsuo Endoh (Tohoku Univ.)
14:50-15:10	Break	
15:10-15:40	Industry-academic collaboration Supersensitive magnetic sensors using ferromagnetic tunnel junctions	Yasuo Ando (Tohoku Univ.)
15:40-16:00	JST ACCEL Three-dimensional integrated circuits technology based on vertical BC-MOSFET and its advanced application exploration	Tetsuo Endoh (Tohoku Univ.)
16:00-16:20	CSTI ImPACT Spintronics integrated circuits	Tetsuo Endoh (Tohoku Univ.)
16:20-16:50	JST OPERA World-leading open innovation platform of fusion technologies bridged IT and transportation system areas	Tetsuo Endoh (Tohoku Univ.)
16:50-17:20	NEDO Next Generation Power Electronics Project to Realize Low-Carbon Society	Tetsuya Suemitsu (Tohoku Univ.)
17:20-17:40	Community-based cooperation project Progress and prospect of CIES's Community-based cooperation project	Yasunobu Mizomoto (Tohoku Univ.)
17:40-17:50	Closing remarks	Tetsuo Endoh (Tohoku Univ.)

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